

This diagram shows a cross-sectional view of a semiconductor device assembly. A substrate 1 is shown with a central region 2. A layer 1a is formed on the substrate 1. A layer 1b is formed on top of layer 1a. A layer 1c is formed on top of layer 1b. A layer 1d is formed on top of layer 1c. A layer 3 is formed on top of layer 1d. A layer 4 is formed on top of layer 3. A layer 5 is formed on top of layer 4. A layer 5a is formed on top of layer 5. A layer 1b is formed on top of layer 1a. A layer 1c is formed on top of layer 1b. A layer 1d is formed on top of layer 1c. A layer 3 is formed on top of layer 1d. A layer 4 is formed on top of layer 3. A layer 5 is formed on top of layer 4. A layer 5a is formed on top of layer 5.

